



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

263

#11

In re the Application of:

James B. Cho, et al.

Serial No.: 09/703,430

Filed: 10/31/00

For: Automatic Bit-Rate Detection Scheme for Use on Sonet Transceiver

Docket No.: TI-3096

Art Unit: 2633

Examiner: TBD

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**LETTER TO THE OFFICIAL DRAFTSPERSON**

Assistant Commissioner for  
Patents  
Washington, D. C. 20231

**MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)**  
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents Application Processing Division's Customer Correction Branch, Washington, D.C. 20231.

J. Dennis Moore, Reg. No. 28,885

1/30/01  
Date

Dear Sir:

Submitted herewith is a set of formal drawings (5 sheets) for the above referenced case. Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments Incorporated. This sheet is enclosed in triplicate.

Respectfully submitted,

J. Dennis Moore  
Attorney for Applicants  
Reg. No. 28,885

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